

FIGURE 1

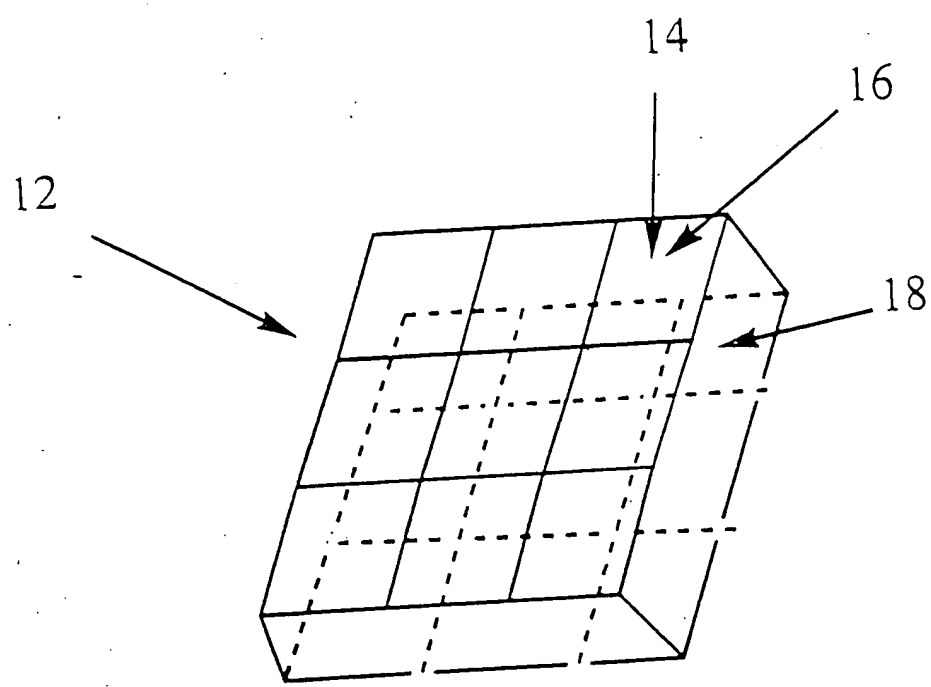


FIGURE 2

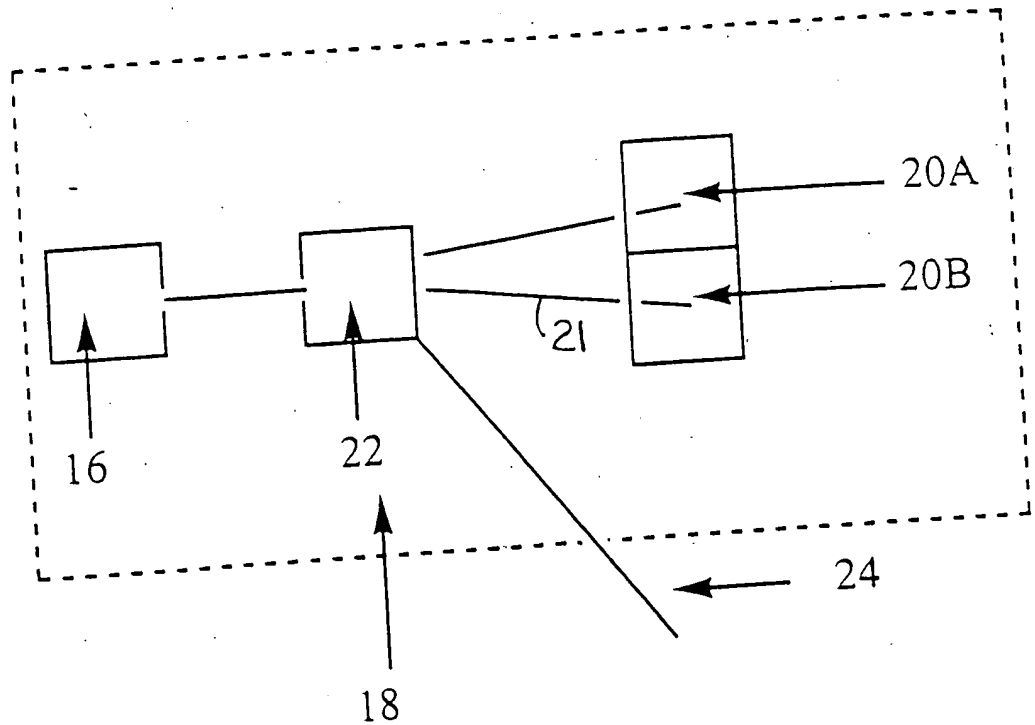


FIGURE 3

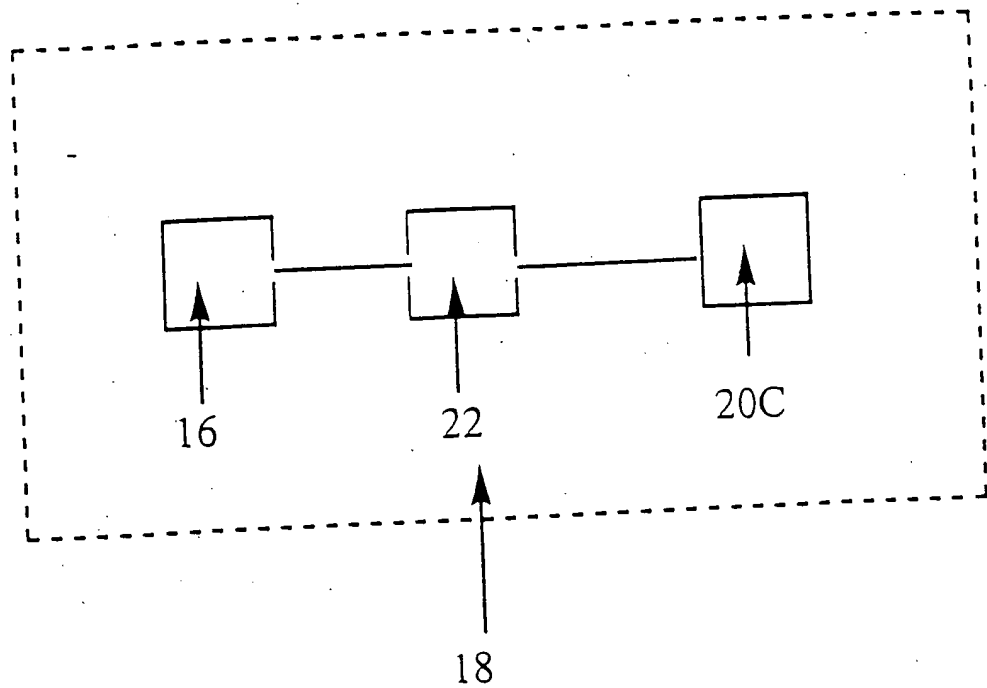


FIGURE 4A

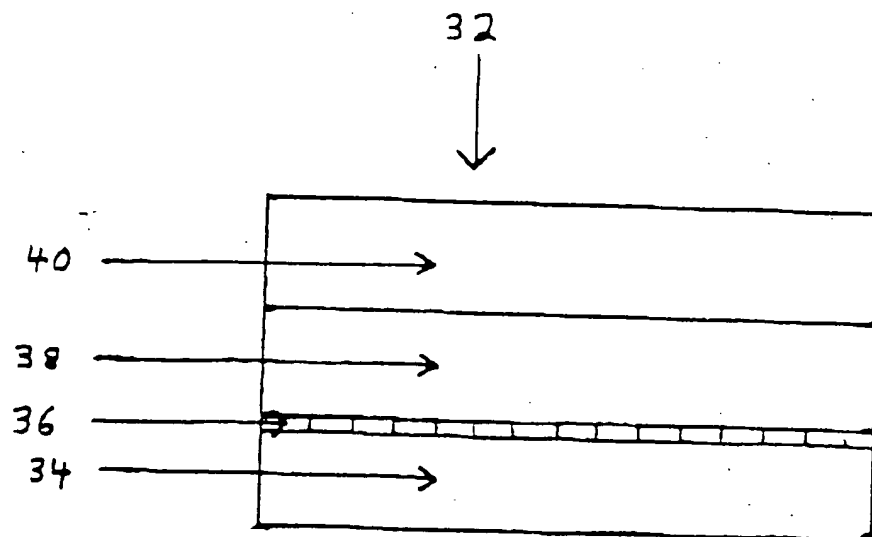


FIGURE 4B

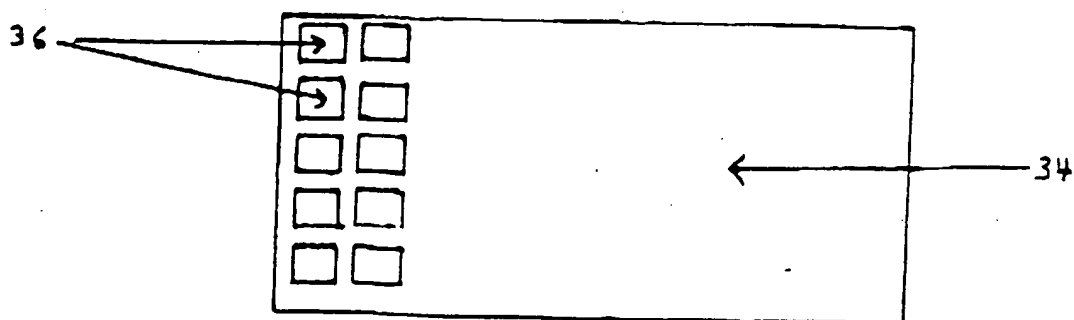


FIGURE 5

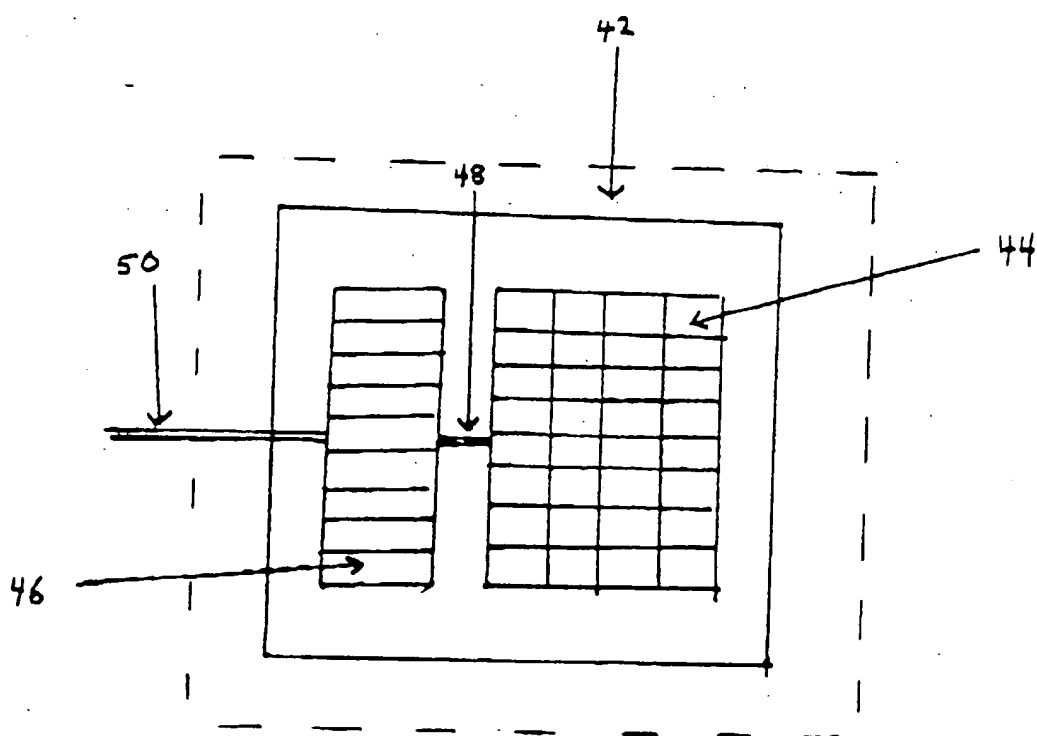


FIGURE 6

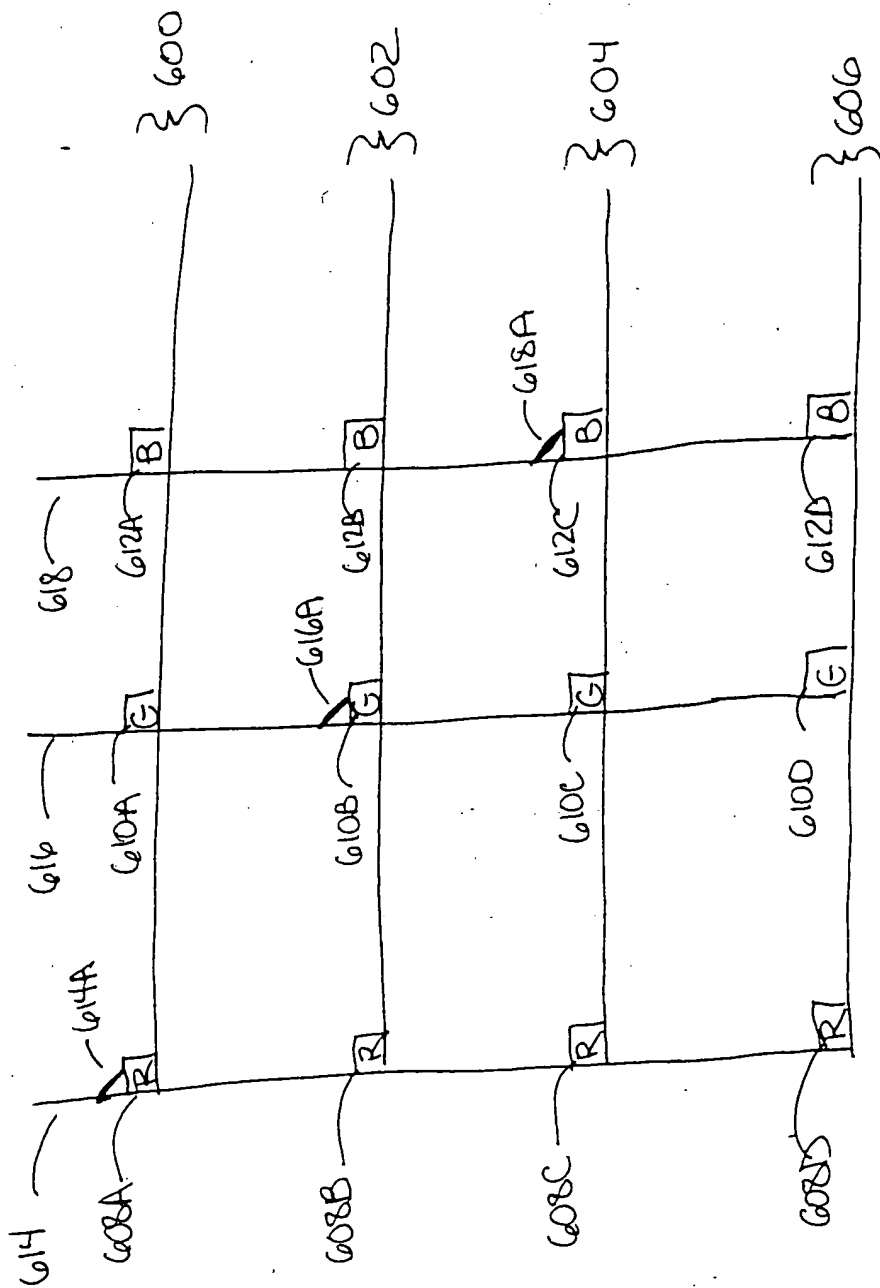


FIG. 7A is a schematic diagram of a device 60 in a perspective view.

FIGURE 7A

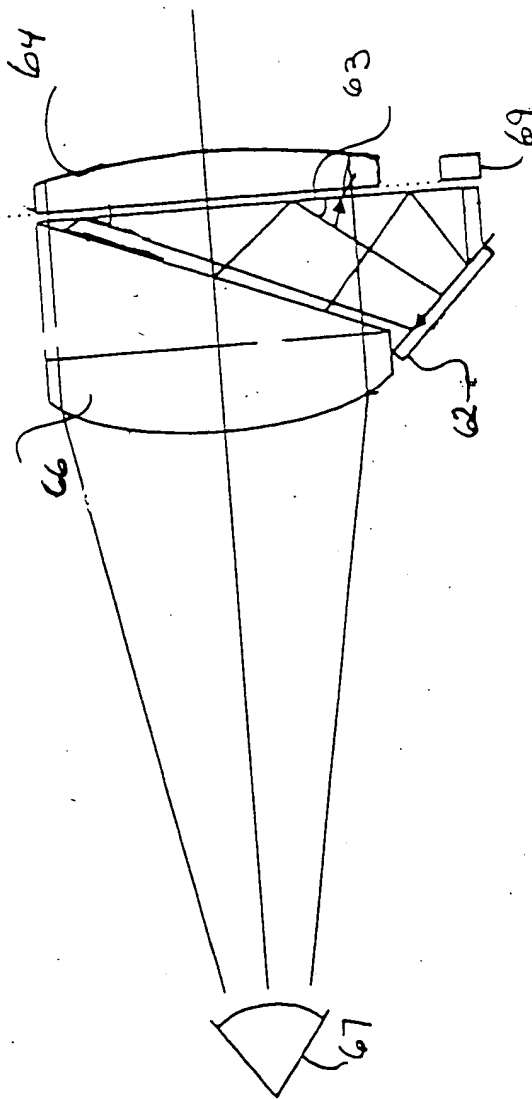


FIGURE 7B

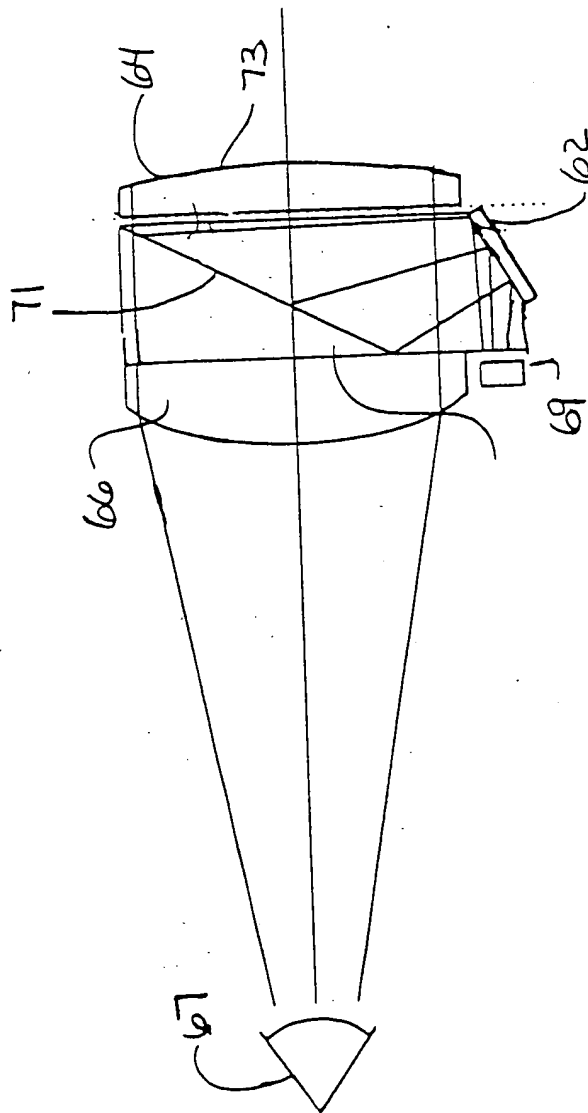


FIG. 7C is a cross-sectional view of the device 100 taken along line 77-77 of FIG. 7A. The device 100 includes a housing 62 and a lens 64. The lens 64 is positioned at the front of the housing 62. The device 100 is configured to receive light 79 and direct it towards the lens 64. The light 79 is shown entering the device 100 from the left and being directed towards the lens 64. The lens 64 is shown as a curved surface. The housing 62 is shown as a rectangular structure. The device 100 is shown in a cross-sectional view.

FIGURE 7C

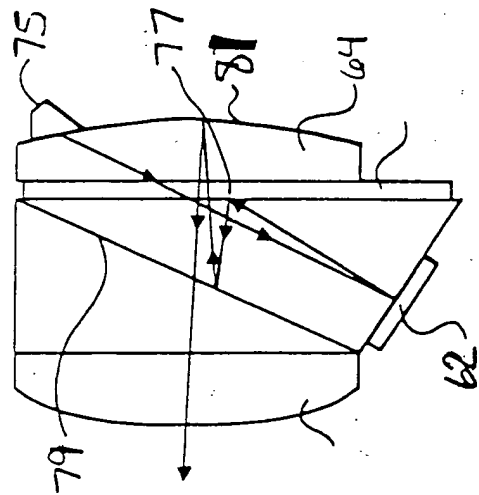


FIG. 7C

FIGURE 8A

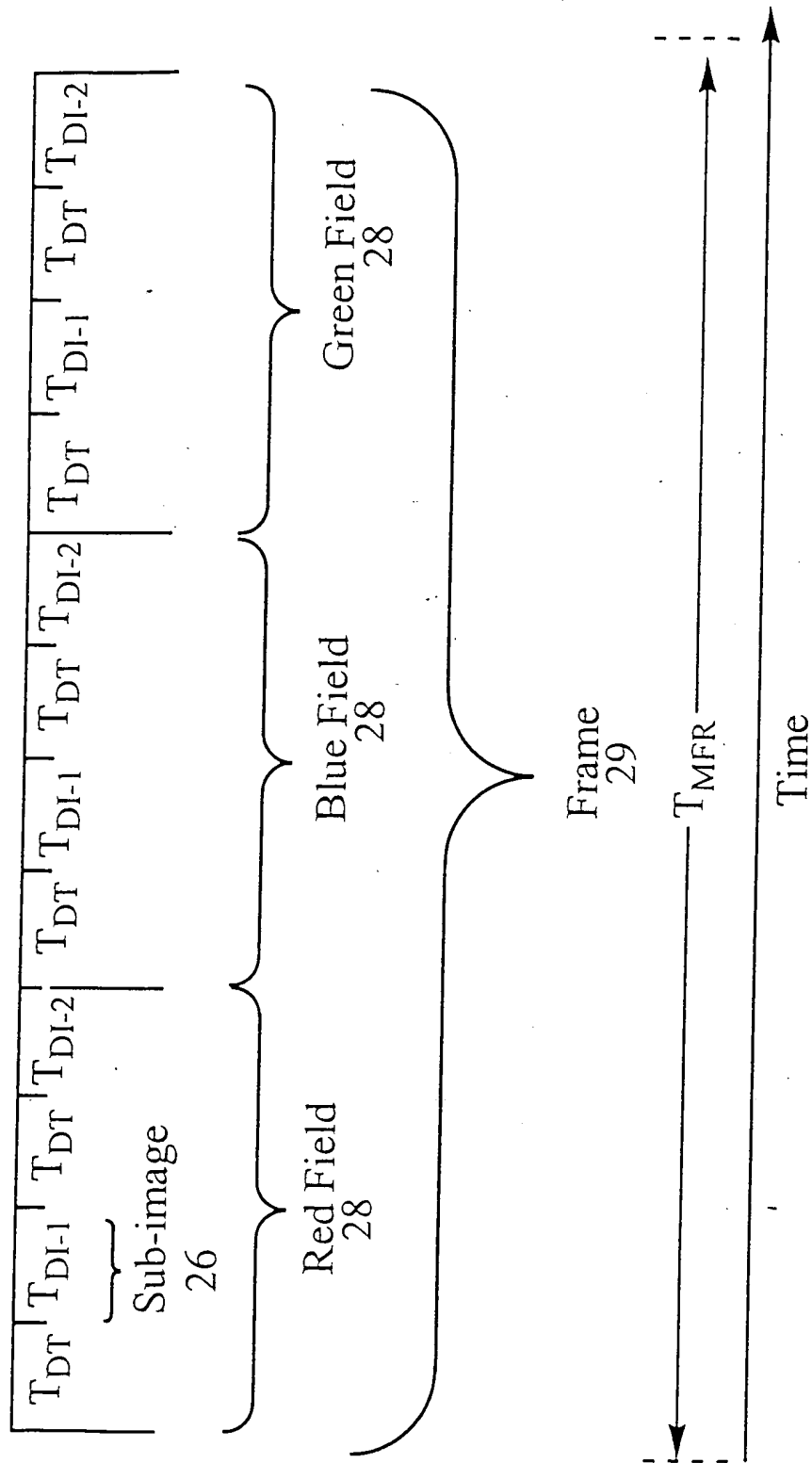


FIGURE 8B

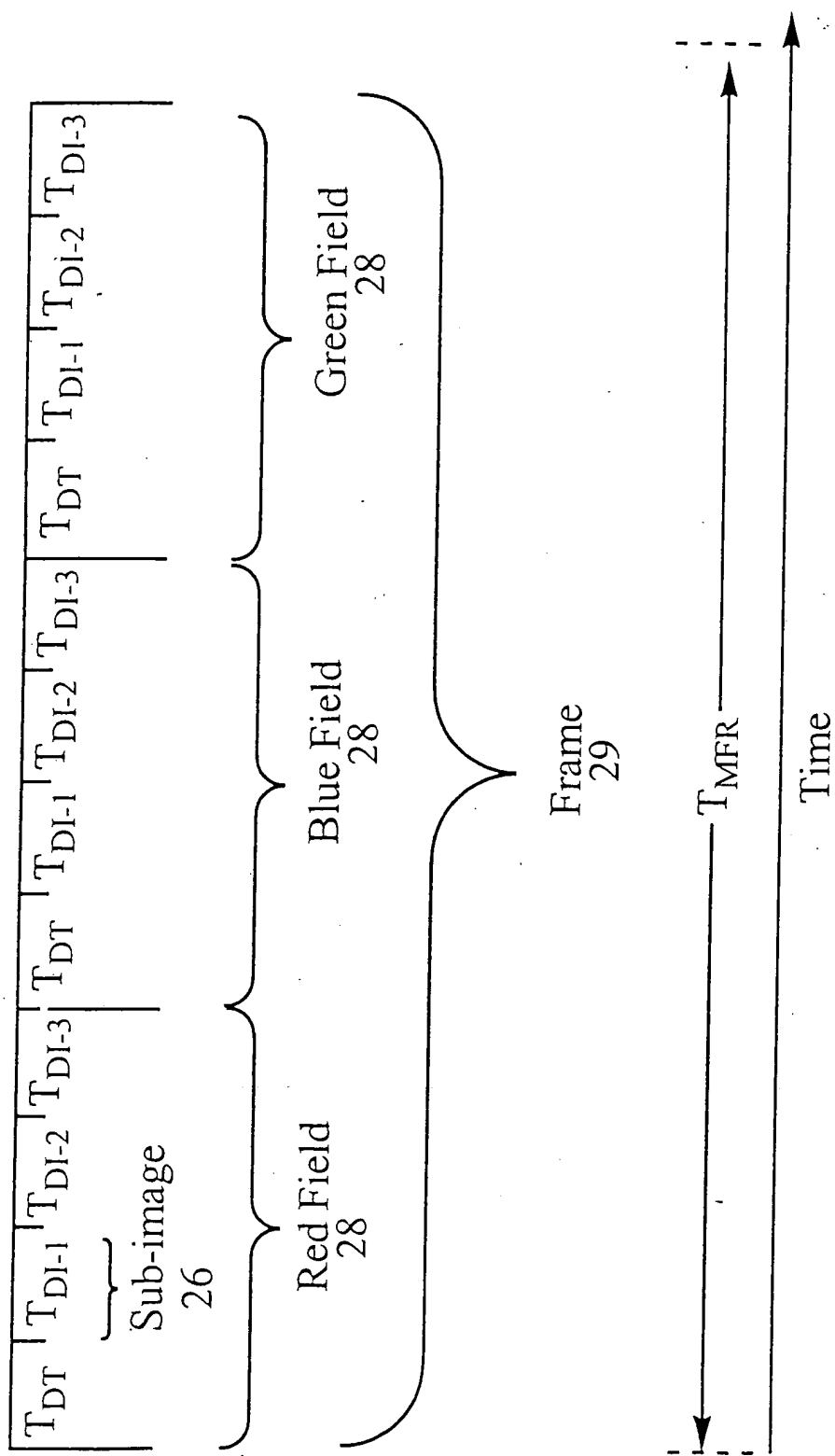


FIGURE 8C

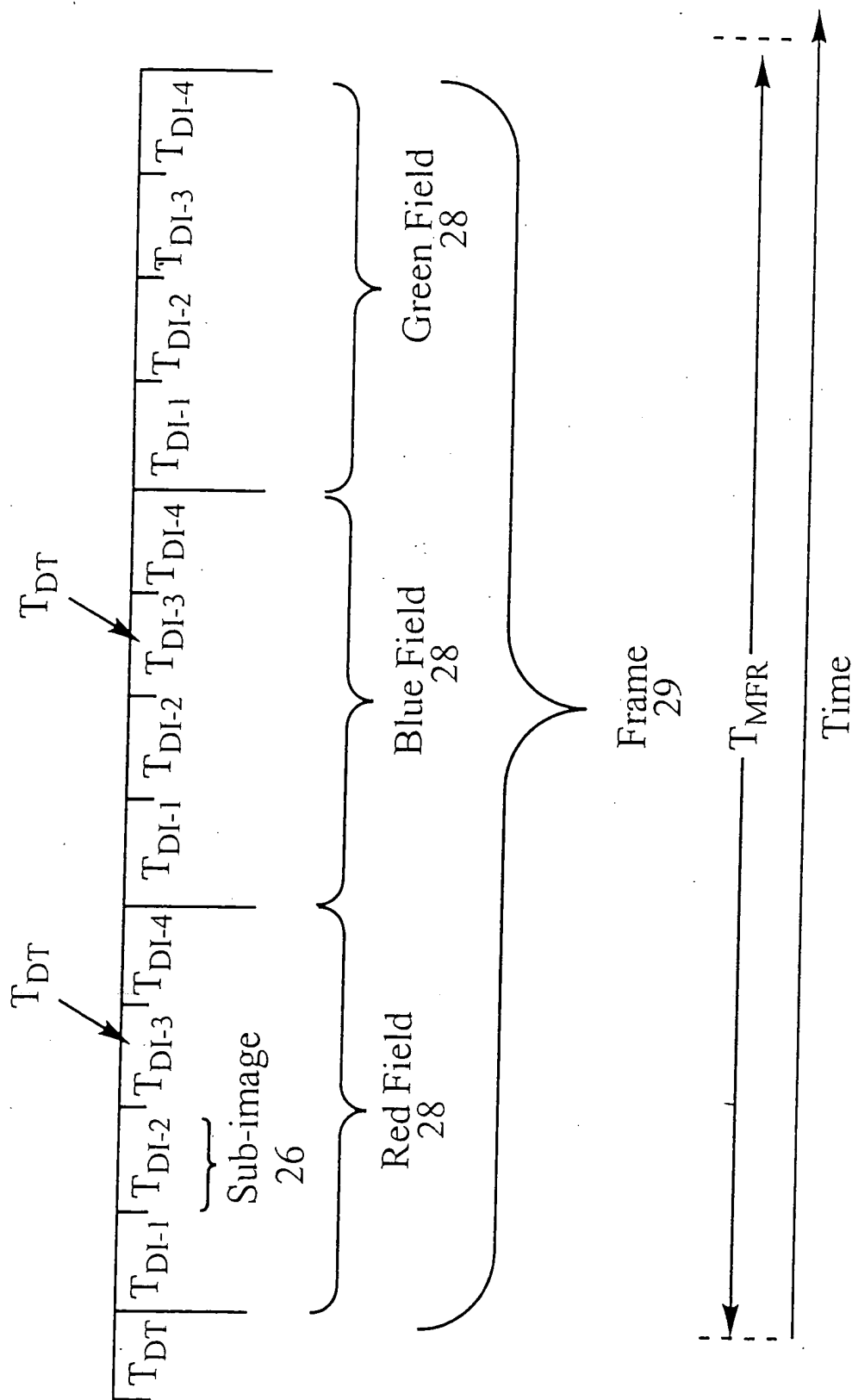
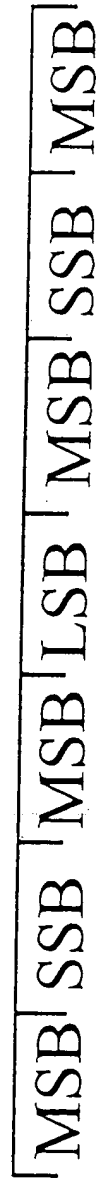


FIGURE 9A



FIGURE 9B



MSB most significant bit
SSB second significant bit
LSB least significant bit

FIGURE 10

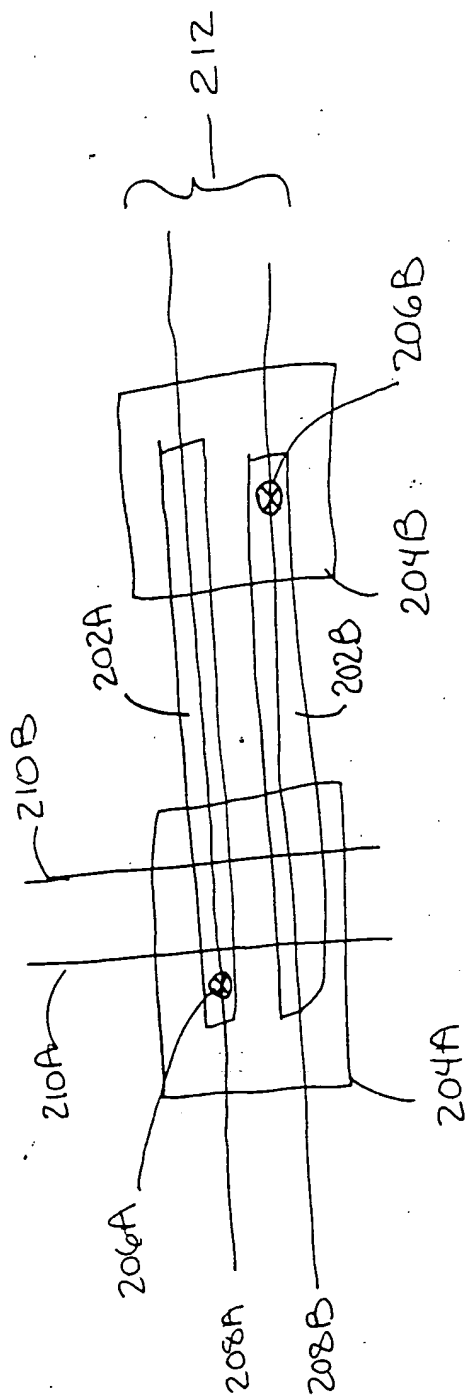


FIGURE 11

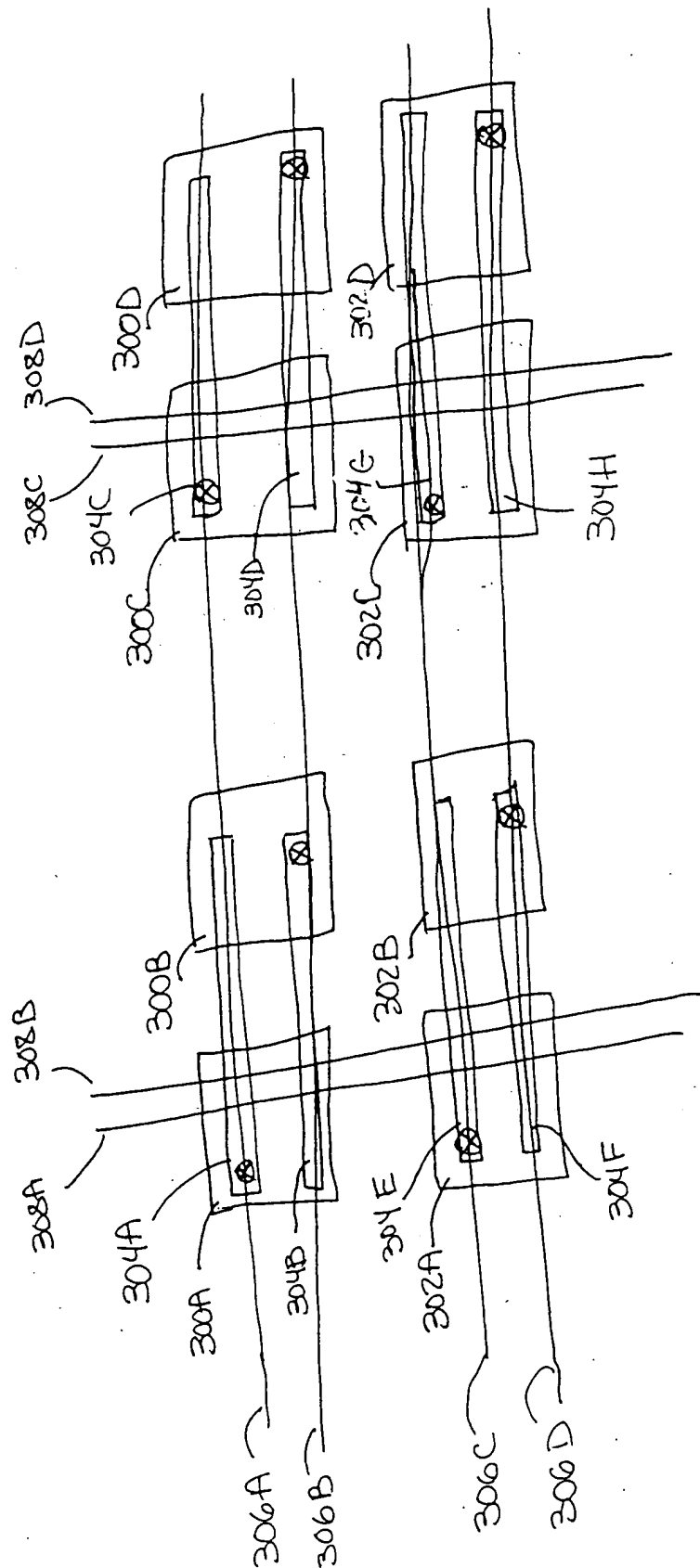


FIGURE 12

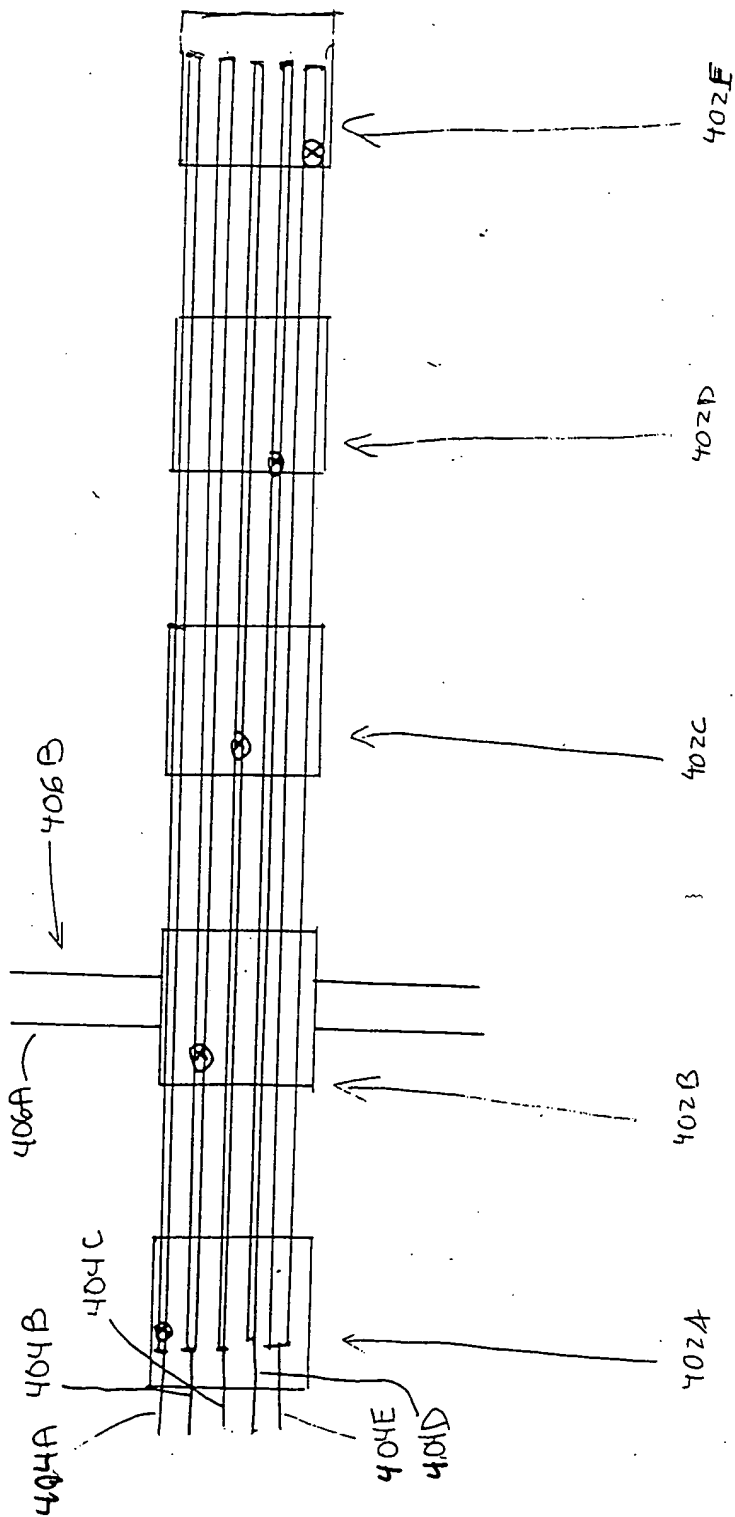


FIGURE 13

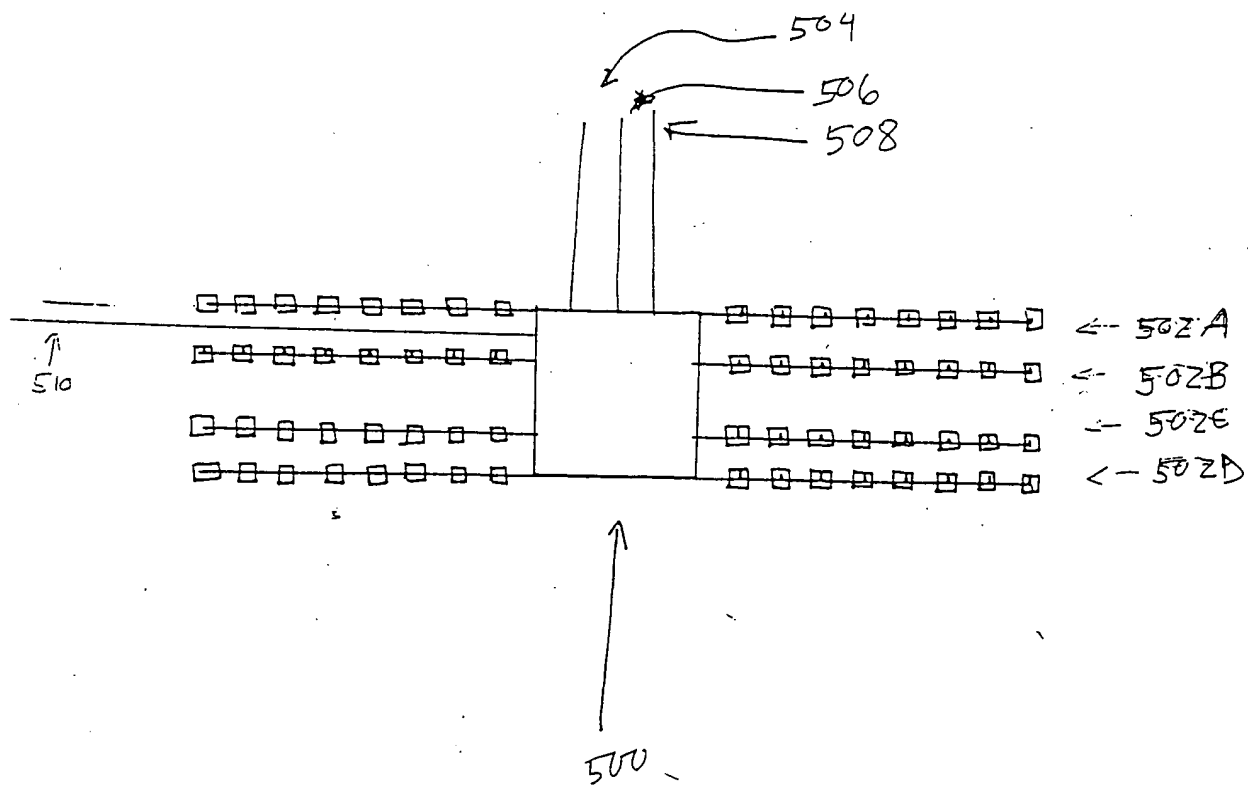


FIGURE 14

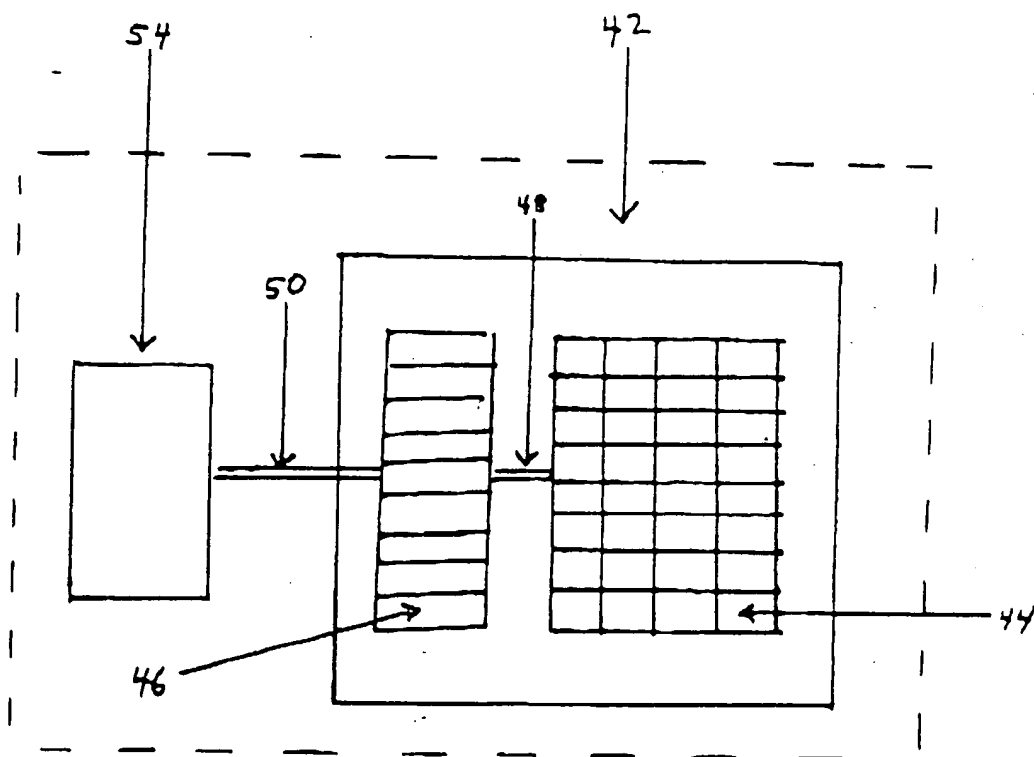
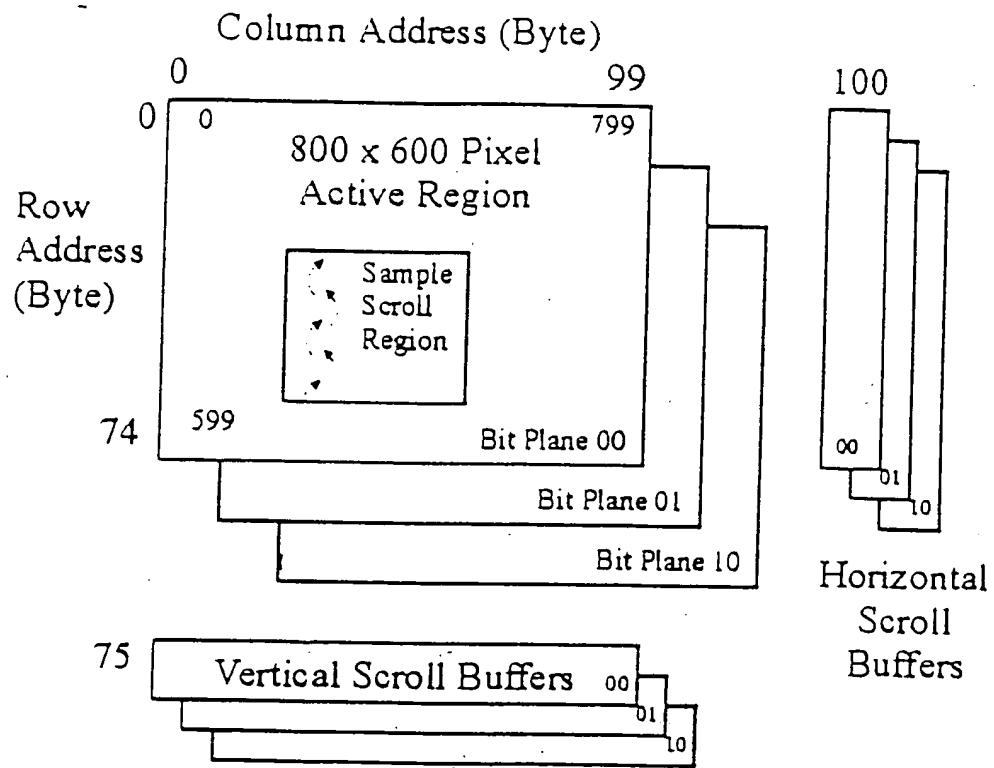
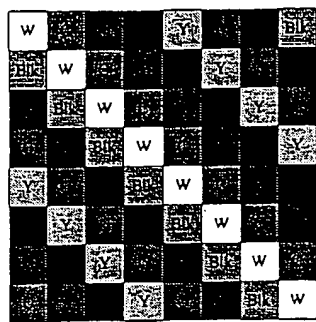


FIGURE 15A

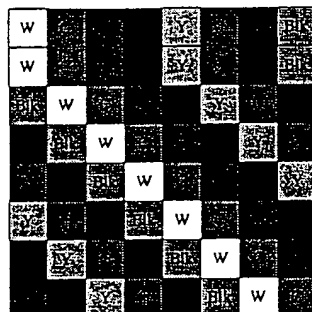




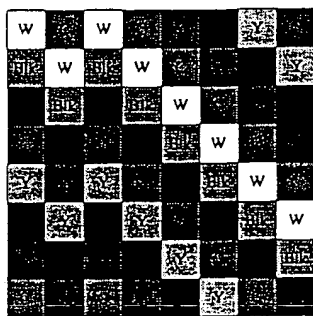
Original Scroll Region

Key

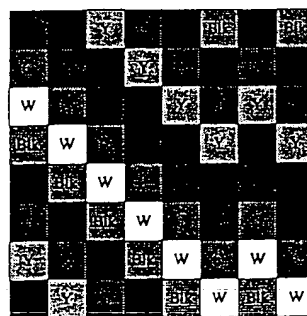
B	G	R	Color	Pixel
0	0	0	White	W
0	0	1	Cyan	
0	1	0	Magenta	
0	1	1	Blue	
1	0	0	Yellow	Y
1	0	1	Green	
1	1	0	Red	
1	1	1	Black	



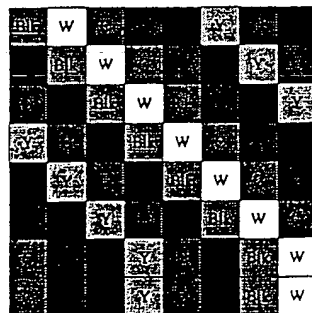
Scroll Up



Scroll Left



Scroll Right



Scroll Down

FIGURE 15B

FIGURE 16

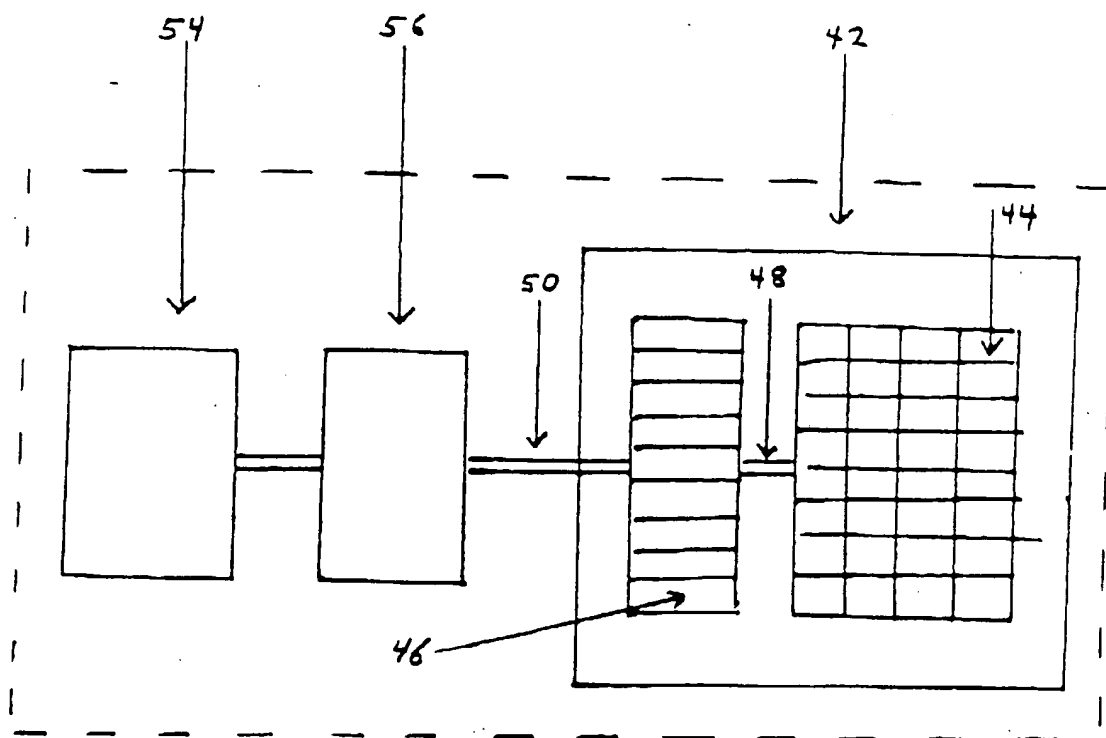


FIGURE 17

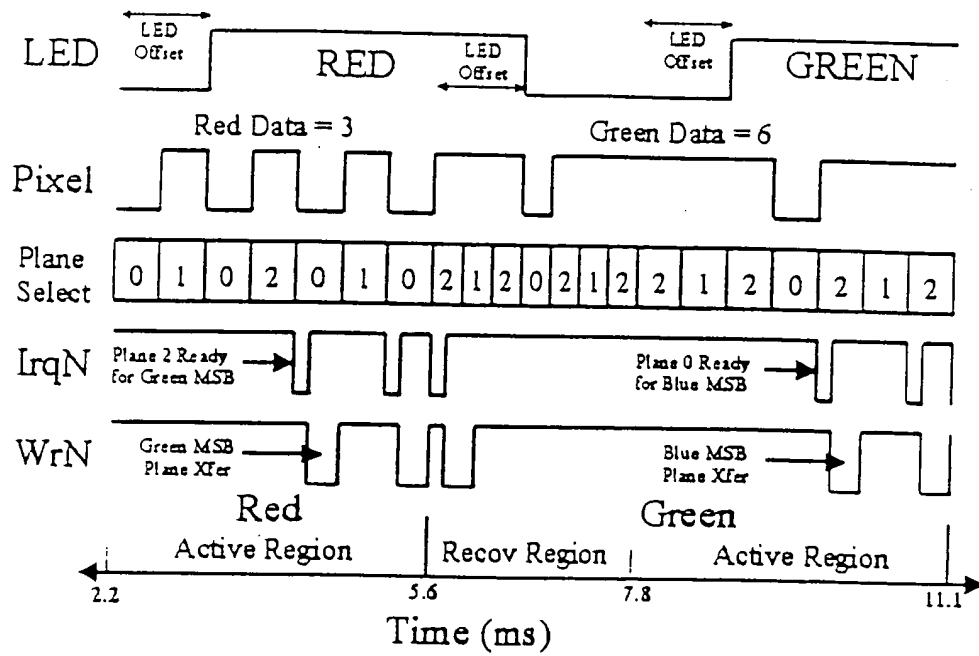
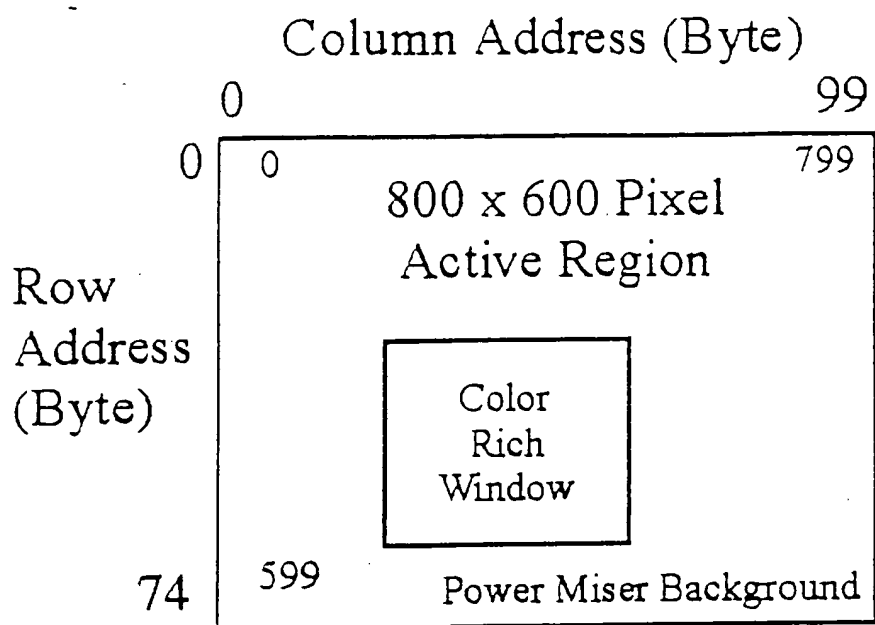


FIGURE 18



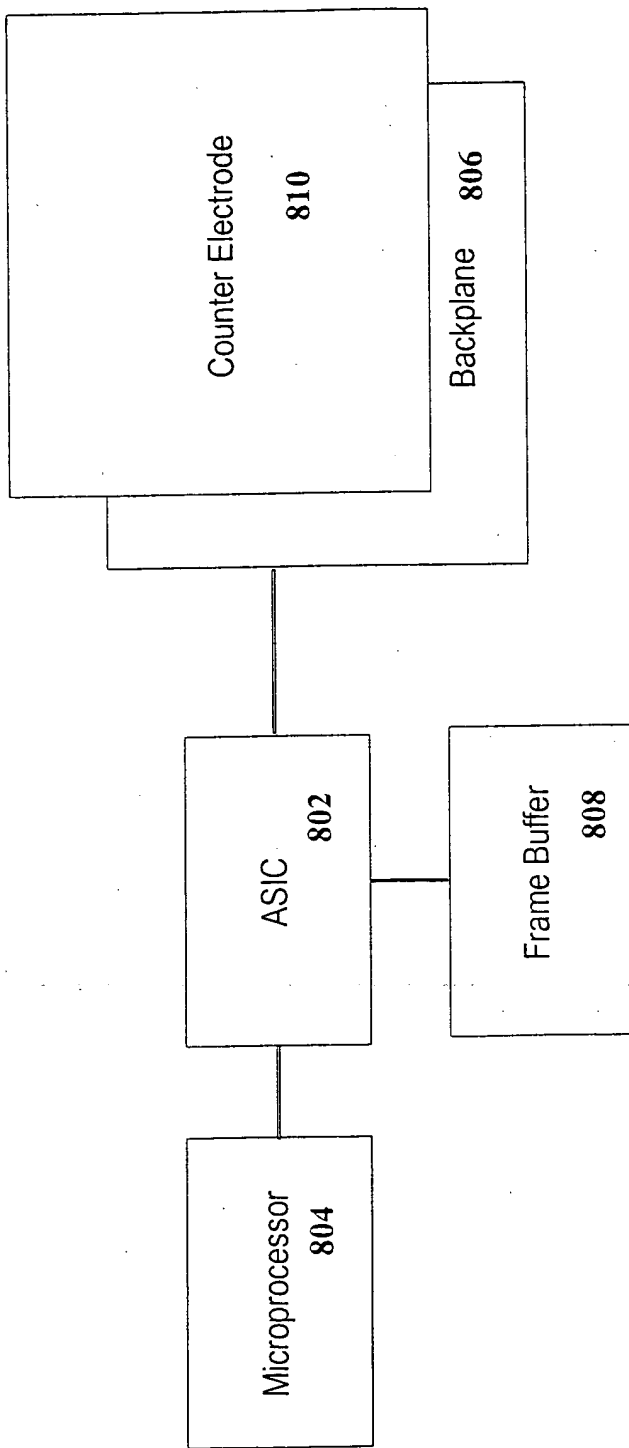


Fig. 19

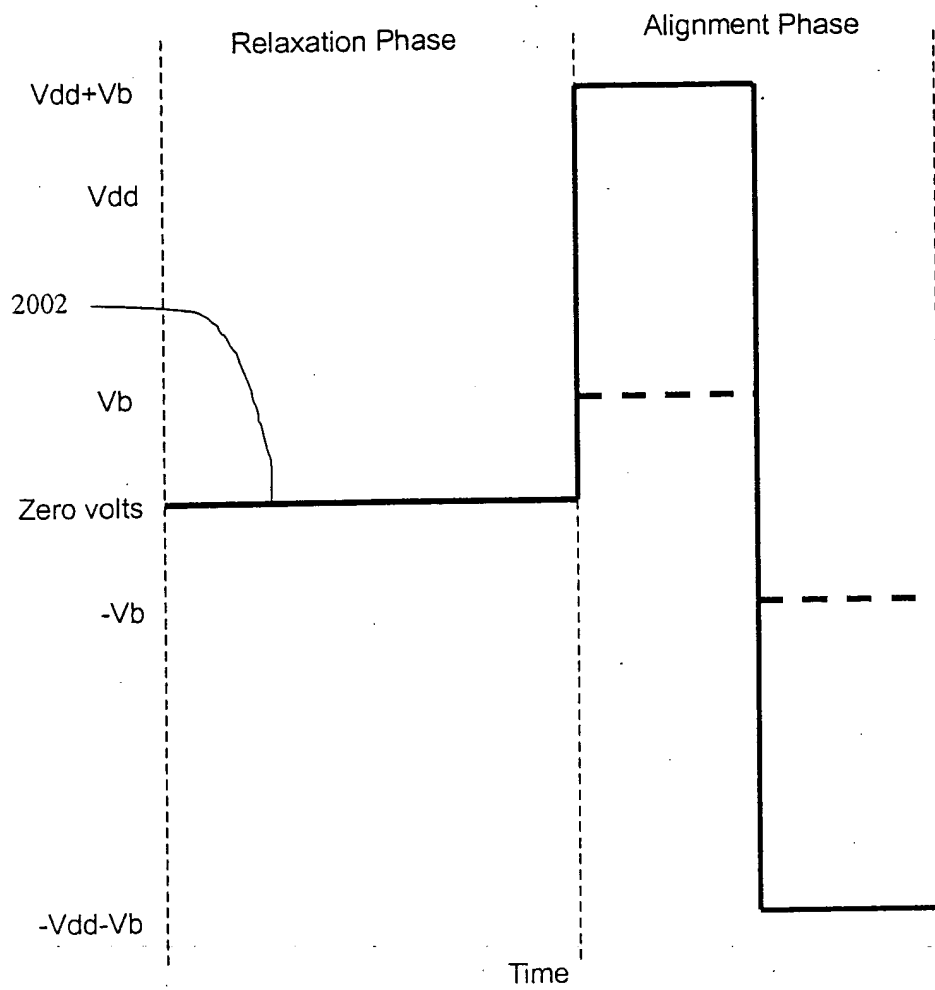


Fig. 20

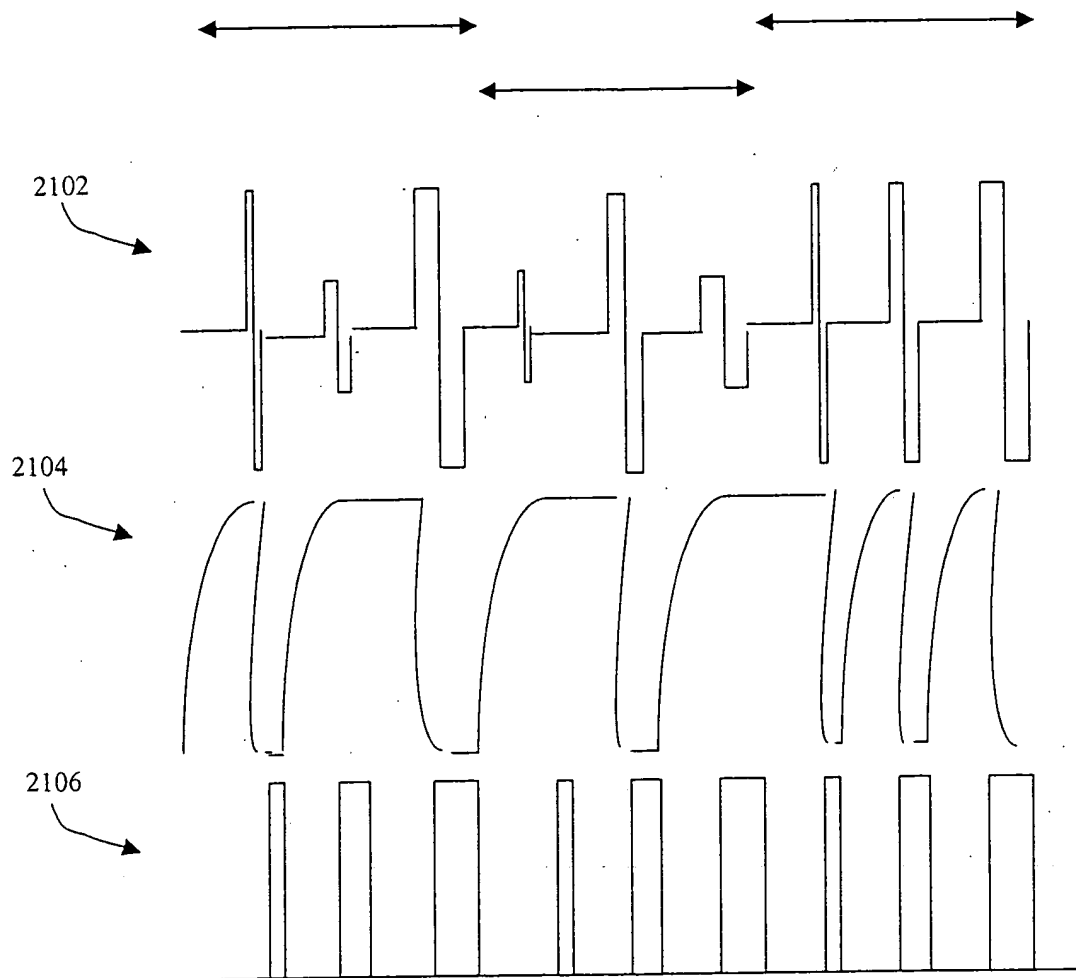


Fig. 21

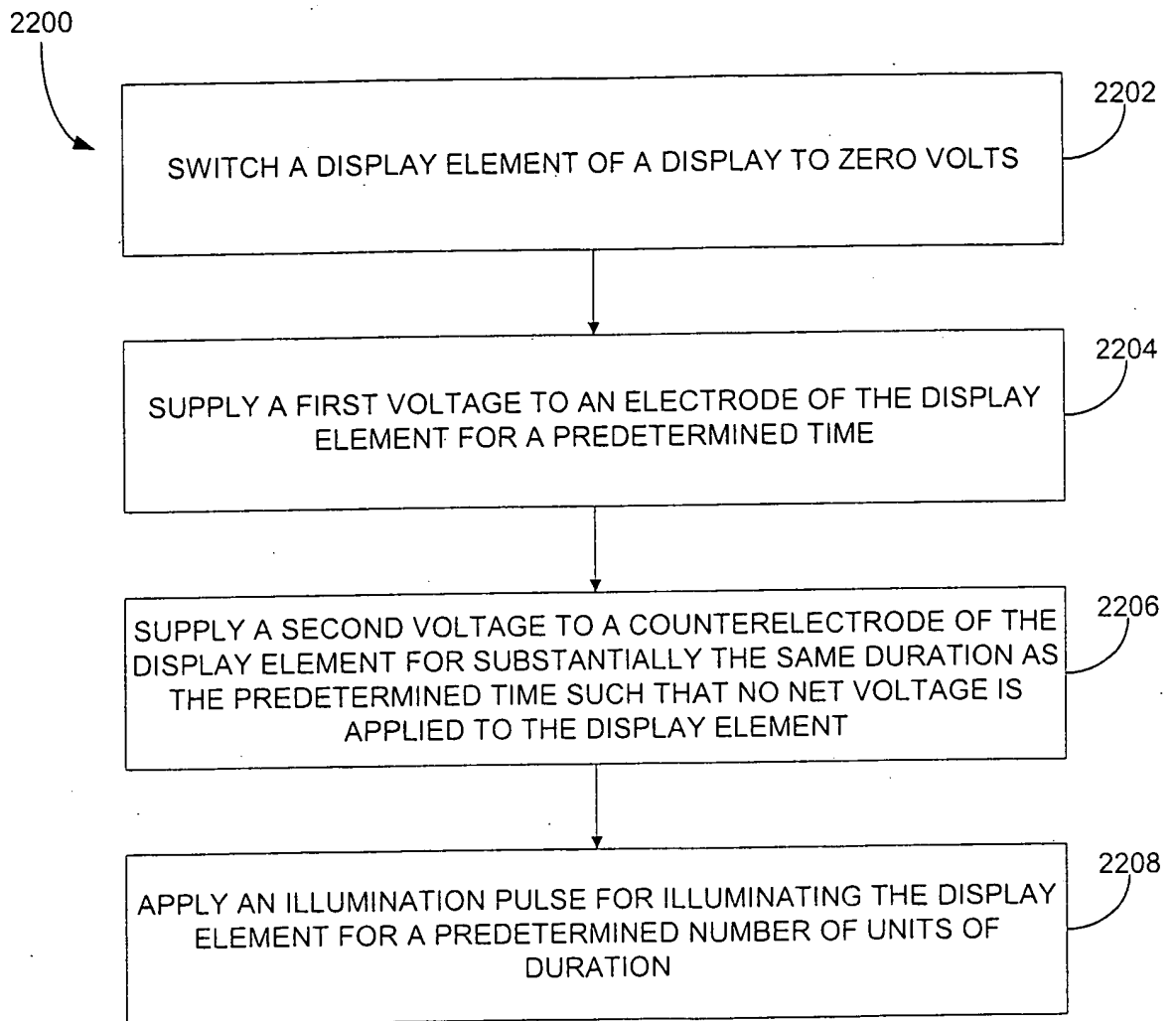


Fig. 22

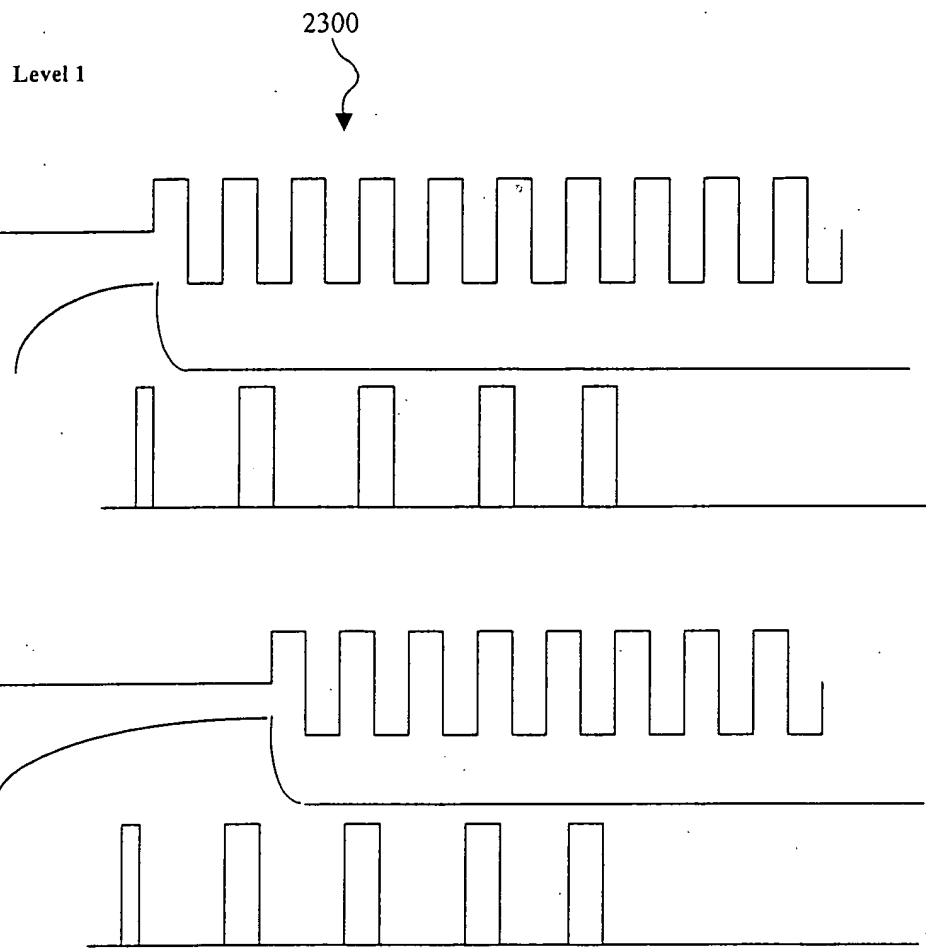


Fig. 23

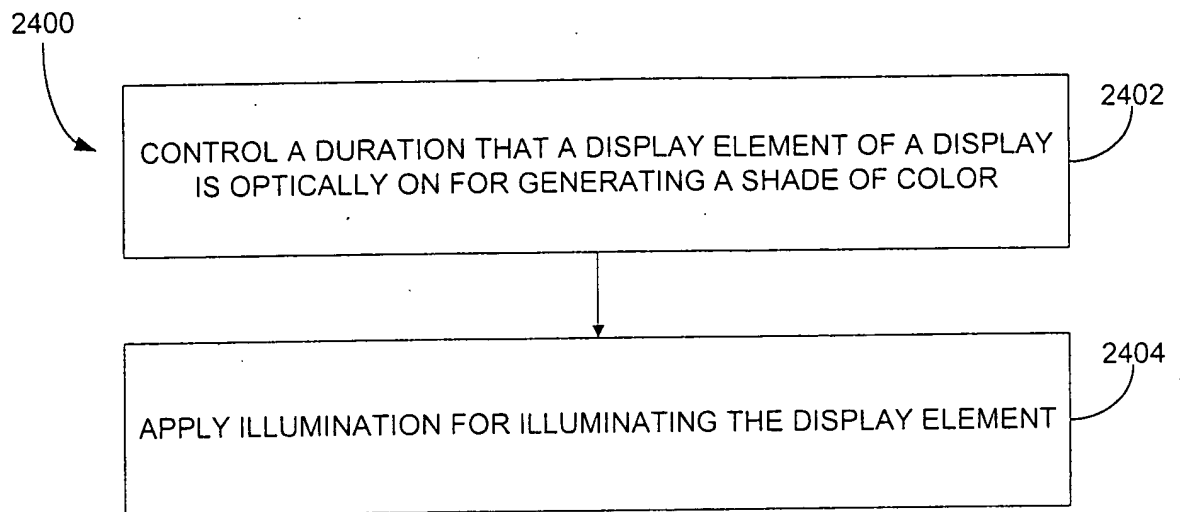


Fig. 24

FIG. 25A is a block diagram of a pixel circuit 2500. The pixel circuit 2500 includes a D/A converter 2502, a MUX 2501, a STORE/EVALUATE block 2504, an op-amp 2506, a LATCH 2510, a LEVEL SHIFTER 2512, and a PIXEL ELECTRODE 2514. The MUX 2501 has a REF INPUT and a DATA INPUT. The D/A converter 2502 is connected to the MUX 2501. The MUX 2501 is connected to the STORE/EVALUATE block 2504. The STORE/EVALUATE block 2504 is connected to the op-amp 2506. The op-amp 2506 is connected to the LATCH 2510. The LATCH 2510 is connected to the LEVEL SHIFTER 2512. The LEVEL SHIFTER 2512 is connected to the PIXEL ELECTRODE 2514.

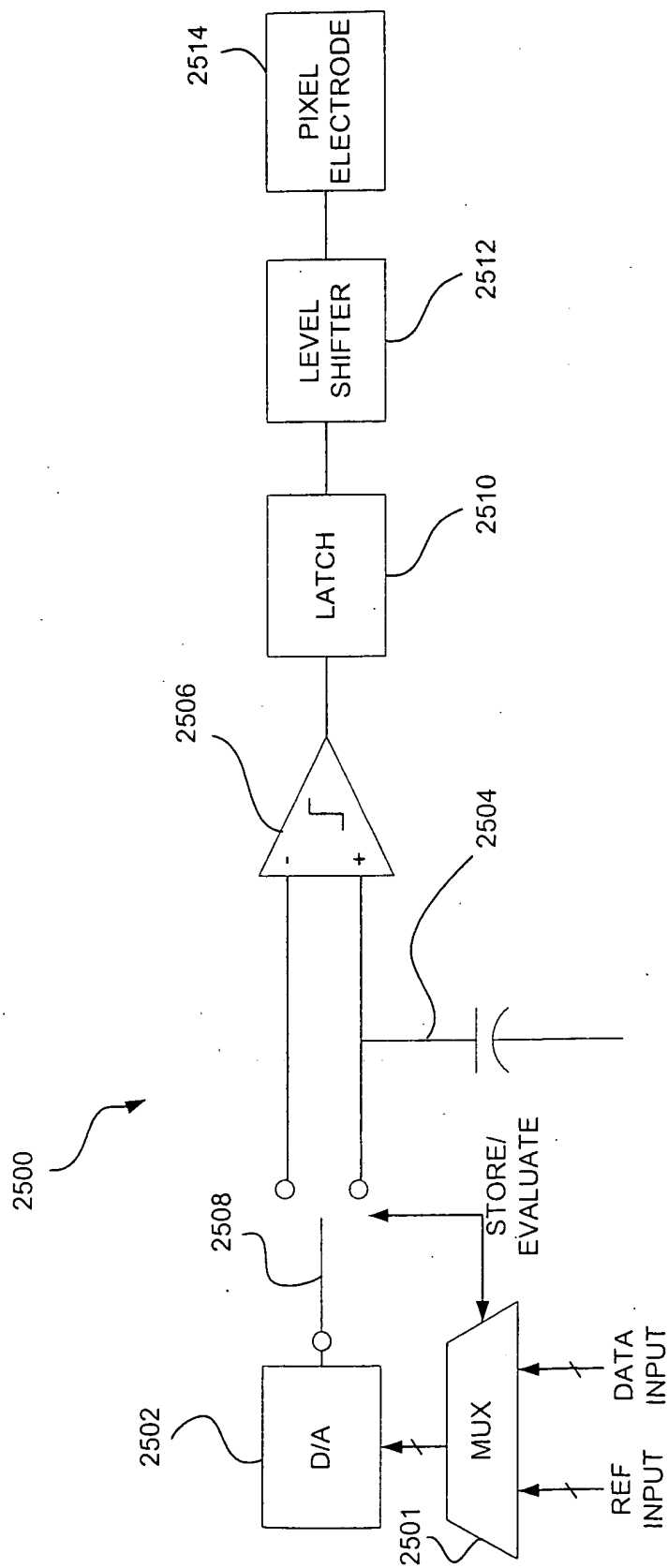


Fig. 25A

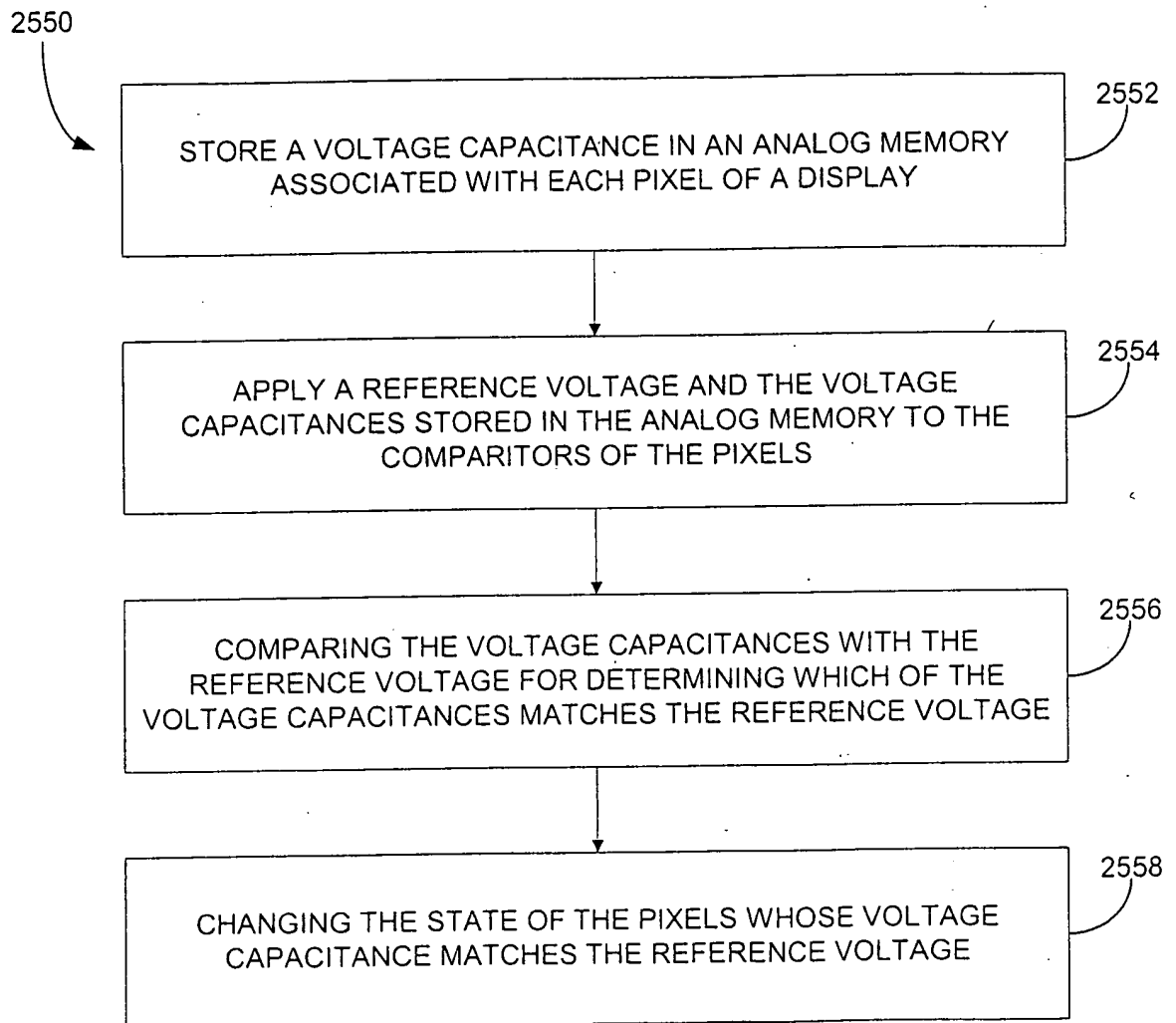


Fig. 25B

Columns

Rows

A	B	A	B
B	A	B	A
A	B	A	B

Fig. 26

Columns

Rows

A	B	C	D	A	B	C	D
C	D	A	B	C	D	A	B
B	A	D	C	B	A	D	C
D	C	B	A	D	C	B	A
A	B	C	D	A	B	C	D
C	D	A	B	C	D	A	B

Fig. 27